



SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)					
				<i>Complete If Known</i>	
				Application Number	10/594,827
				Filing Date	September 28, 2006
				First Named Inventor	Monferrer, Pedro
				Art Unit	Unknown
				Examiner Name	Unknown
Sheet	1	of	1	Attorney Docket No: P23882	

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Examiner Initials*	Cite No	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of cited Document	T ²
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		WO-2004084070A1	09/30/2004	AUSTIN, T. , et al.	
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-Issue number(s), publisher, city and/or country where published.	T ²
		QIANG, W. et al., "Voltage and Frequency Control With Adaptive Reaction Time in Multi-Clock-Domain Processors", <u>High-Performance Computer Architecture, HPCA-11</u> , 11th International Symposium on San Francisco, (Feb. 2005), pages 178-189.	
		<u>International Search Report and Written opinion for application no. PCT/ES2005/070152</u> mailed on 05/12/2006, 12 pgs.	

EXAMINER

DATE CONSIDERED